INTELLEC SERIES II
MICROCOMPUTER
DEVELOPMENT SYSTEM
DOUBLE-DENSITY
DISKETTE SUBSYSTEM

schematic
drawings
CONTENTS

INTEL/® SERIES II
MICROCOMPUTER
DEVELOPMENT SYSTEM
DOUBLE-DENSITY
DISKETTE SUBSYSTEM

Manual Order Number: 980425-01 Rev. B

Additional copies of this manual or other Intel literature may be obtained from:

Literature Department
Intel Corporation
3065 Bowers Avenue
Santa Clara, CA 95051

The information in this document is subject to change without notice.

Intel Corporation makes no warranties of any kind with regard to this material, including, but not limited to, the implied warranties of merchantability and fitness for a particular purpose. Intel Corporation assumes no responsibility for any errors that may appear in this document. Intel Corporation makes no commitment to update nor to keep current the information contained in this document.

Intel Corporation makes no responsibility for the use of any circuitry other than circuitry embodied in an Intel product. No other circuit patent licenses are implied.

Intel software products are copyrighted by and shall remain the properties of Intel Corporation. Use, duplication or disclosure is subject to restrictions stated in Intel's software license, or as defined in ASPR 7-024.904.

No part of this document may be copied or reproduced in any form or by any means without the prior written consent of Intel Corporation.

The following are trademarks of Intel Corporation and may be used only to identify Intel products:

SNP      Intel     Math
LXT    IRC      Matrox
ITY      ISK      Multitech
ISB       TSI    Microelectronics
ISB       TSI
ISB       TSI    VLSI System
ICS       TSI

and the combination of ICS, ISB, ISBX, MS, or RMX and a numerical suffix.

Copyright © 1990 Intel Corporation
Intel Corporation, 3065 Bowers Avenue, Santa Clara, California 95051
**ELECTRICAL SPECs:**

1. **INPUT:** 100-15, 20, 230 VAC ± 10%, 47-64 Hz.
2. **OUTPUT:**
   2.1 5V DC, nominal adjustable ±5% to be set at nominal ±1% max load: 100mA, 3.6V.
   line load regulation combined 1.0%.
   max. current: 40mA, 0.85 VDC.
   transient response: 50 μsec max for a 50% load change.
   over voltage protection: 6.6V to 6.6V.
   over current foldback protection 1% to 50% overload.

2.2 -5V DC nominal adjustable ±5% to be set at nominal ±1% max load: -3.6V, 3.6A.
line load regulation combined 1.0%.
max. current: 40mA, 0.85 VDC.
transient response: 50 μsec max for a 50% load change.
short circuit current shall not exceed 2.5 A.

3. **4V DC, nominal adjustable ±5% to be set at nominal ±1% max load: ±4V, 2.5 A.
line load regulation combined: 1.0%.
max. current: 100mA, 0.25 VDC.
over current foldback protection: 5% to 50% overload.
separate ground return provided, connected to electronic ground at one point.

4. A single green wire with yellow stripe will connect VS 6F connector frame ground to chassis.

5. All electronic grounds (+5V,-5V,±15V) shall be common and isolated from frame ground.

6. Temperature coefficient: ±0.02% per °C max.

7. Over voltage protection need not be adjustable.

8. **LINE FILTER REQUIRED.**

9. **ENVIRONMENT:**
   - Ambient temp 0°C to 45°C,
   - Humidity: 90% with no condensation.

10. Life expectancy: 10 years.

11. **THIS POWER SUPPLY TO PASS ELECTROSTATIC DISCHARGE TESTS PER INTEL SPECIFICATION 440001 AT A LEVEL OF 15 KILOVOLTS.**

---

**ELECTRICAL SPECs (CONT.)**

12. **EFFICIENCY SHALL BE 90% OR GREATER AT FULL LOAD.**

13. **VOLTAGE ADJUSTMENT POTENTIOMETERS SHALL BE ACCESSIBLE FROM THE TOP OF UNIT.**

14. **ALL MEASUREMENTS TO BE MADE AT THE END OF THE APPROPRIATE POWER CABLE CONNECTOR.**

---

**NOTES:**

- **MULTIPLE PIECE CHASSIS CONSTRUCTION ACCEPTABLE.**
- **MARK NOMENCLATURE AS SHOWN IN PERMANENT CONTRASTING INK.**
- **REMOVED.**
- **PROVIDE 2 AMP, NORMAL BLOW, FUSE, ITEM 6, IN 15A FUSE BOX, ITEM 6.**
- **ALL LARGE CAPACITORS MUST BE SECURELY FASTENED TO CHASSIS.**
- **100% BENDING SURFACE REQD BEHIND HARDWARE LOCKING DEVICES.**
- **STRAIN RELIEF REQD ON ALL CABLES EXTENDING BEYOND ENVELOPE.**
- **MARK VENDOR ID WITH CONTRASTING P/R. COLOR; APPROX. WHERE SHOWN.**
- **PART NO. 15. 450-049B-03 SPEC CONTROL. DRAWING AND LIST OF VENDORS ARE TRACKING DOCUMENTS.**

**FINISH ALODINE 1000, CLEAR.**
MARK VENDOR ID WITH CONTRASTING PERN COLOR APPROX WHERE SHOWN.

MARK PART NO & REV LEVEL WITH CONTRASTING PERN COLOR, .12 HIGH, APPROX WHERE SHOWN.

WORKMANSHIP PER MCS5 C5010-0001-001.
1. ASSY PART NO. IS L002292-01. ASSY AND PL ARE TRACKING DOCUMENTS.

NOTES: UNLESS OTHERWISE SPECIFIED.
5. SHIELD CABLE TO BE ROLLED INTO SINGLE CONDUCTOR AND SLEEVED AS SHOWN.

4. MARK VENDOR ID WITH CONTRASTING PERM COLOR, APPROPRIATE TO SHOWN.

3. MARK PART NO AND REV LEVEL WITH CONTRASTING PERM COLOR, IE WHITE, APPROPRIATE TO SHOWN.

2. WORKMANSHIP PER VCSO GANS P9-000P-00.

1. ASSEMBLY PART NO IS 4602176-01.

NOTES: UNLESS OTHERWISE SPECIFIED.